AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 5, line 11 as follows:

In another aspect of the present invention, a method of updating a design of a semiconductor chip at a hardware ~~design~~ <u>description</u> language ~~level~~ (HDL) of simulation, includes steps of automatically reading and setting a value of control signals on a per cycle basis in a template and updating the HDL design with new data, changing a first predetermined value of the template to be set with the previous state of the control signal, and executing a test sweep to determine "don't care" states of control signals.